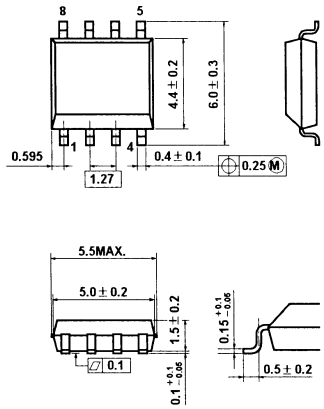
TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT8G131

Strobe Flash Applications

- Supplied in Compact and Thin Package Requires Only a Small Mounting Area
- 4th generation (trench gate structure) IGBT
- Enhancement-mode
- 4-V gate drive voltage: V_{GE} = 4.0 V (min) (@I_C = 150 A)
- Peak collector current: I_C = 150 A (max)

Unit: mm



1, 2, 3 EMITTER
4 GATE
5, 6, 7, 8 COLLECTOR

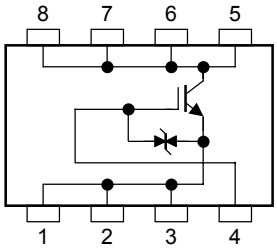
JEDEC	—
JEITA	—
TOSHIBA	2-6J1C

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Collector-emitter voltage		V_{CES}	400	V
Gate-emitter voltage	DC	V_{GES}	±6	V
	Pulse	V_{GES}	±8	
Collector current	DC	I_C	8	A
	1 ms	I_{CP}	150	
Collector power dissipation (Note 1)		P_C	1.1	W
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55~150	°C

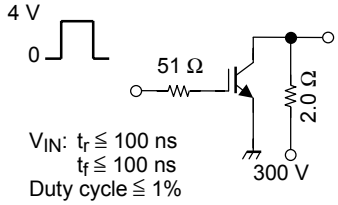
Note 1: Drive operation: Mount on glass epoxy board [1 $inch^2$ × 1.5 t]

Equivalent Circuit



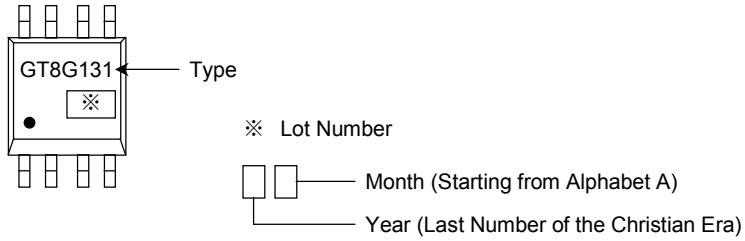
These devices are MOS type. Users should follow proper ESD handling procedures.
Operating condition of turn-off dv/dt should be lower than 400 V/μs.

Electrical Characteristics (Ta = 25°C)

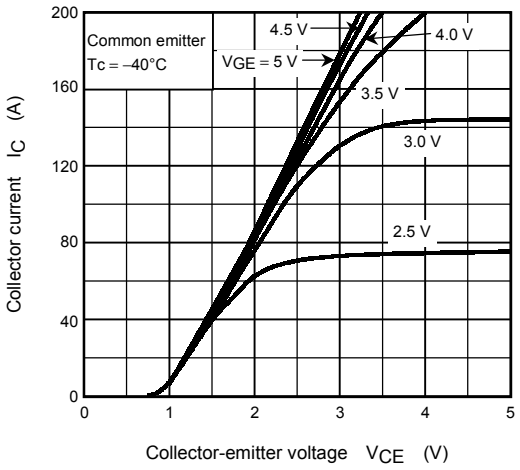
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GES}	V_{GE} = ±6 V, V_{CE} = 0	—	—	±10	μA
Collector cut-off current		I_{CES}	V_{CE} = 400 V, V_{GE} = 0	—	—	10	μA
Gate-emitter cut-off voltage		$V_{GE\ (OFF)}$	I_C = 1 mA, V_{CE} = 5 V	0.6	—	1.5	V
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	I_C = 150 A, V_{GE} = 4 V	—	3.0	7.0	V
Input capacitance		C_{ies}	V_{CE} = 10 V, V_{GE} = 0, f = 1 MHz	—	3800	—	pF
Switching time	Rise time	t_r		—	1.5	—	μs
	Turn-on time	t_{on}		—	1.7	—	
	Fall time	t_f		—	1.9	—	
	Turn-off time	t_{off}		—	2.4	—	
Thermal resistance	(Note 2)	$R_{th\ (j\text{-}a)}$	—	—	—	114	°C/W

Note 2: Drive operation: Mount on glass epoxy board [1 $inch^2$ × 1.5 t]

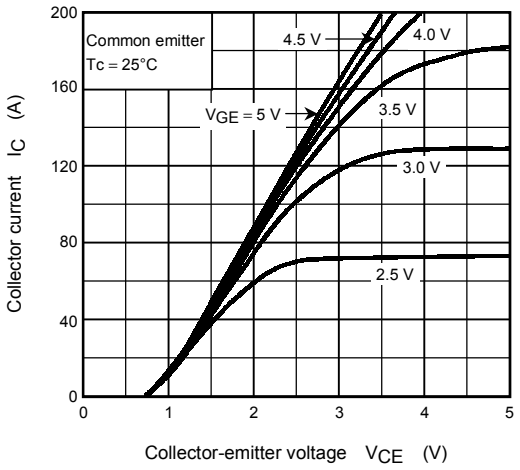
Marking



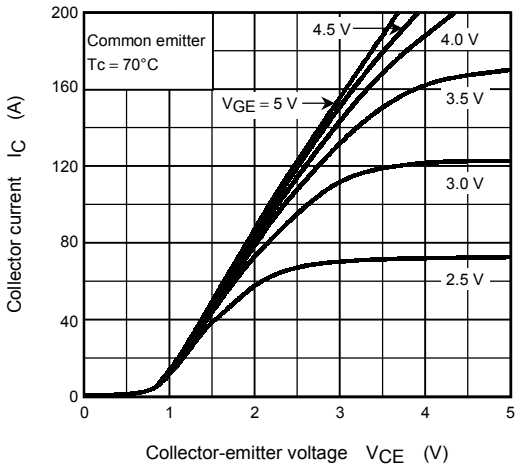
$I_C - V_{CE}$



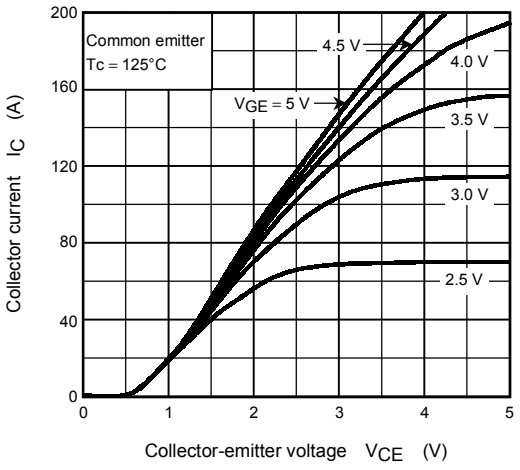
$I_C - V_{CE}$



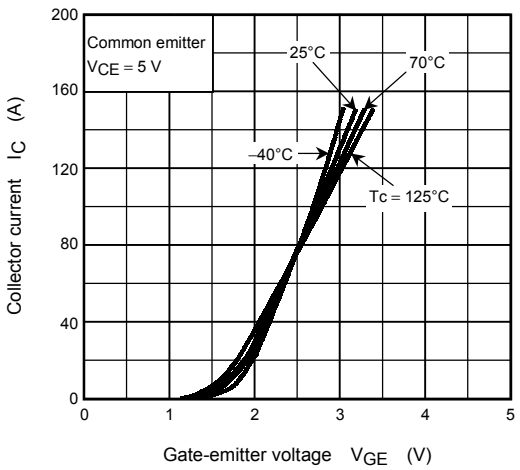
$I_C - V_{CE}$



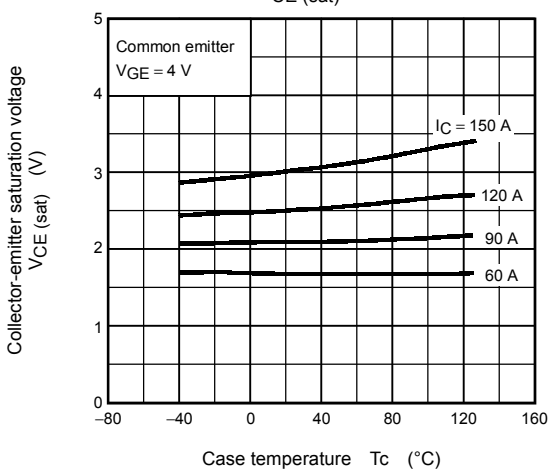
$I_C - V_{CE}$



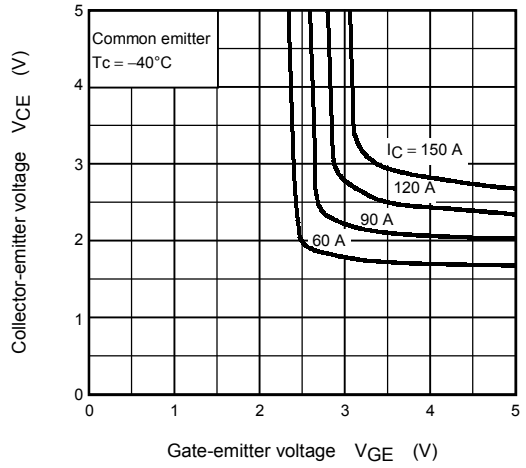
$I_C - V_{GE}$



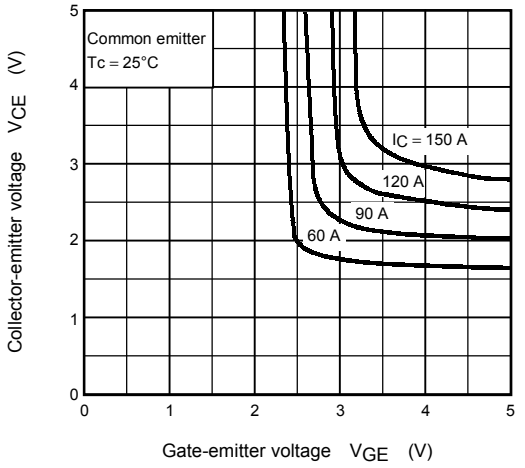
$V_{CE\ (sat)} - Tc$



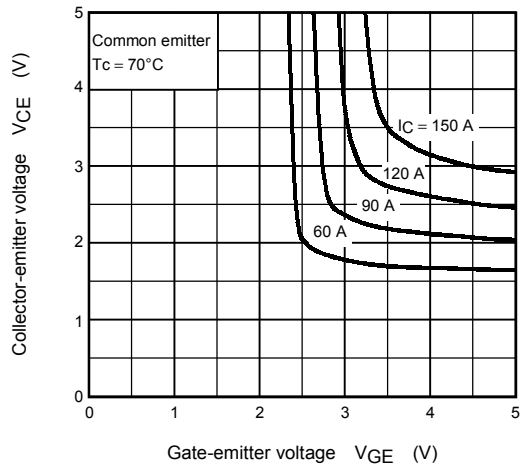
$V_{CE} - V_{GE}$



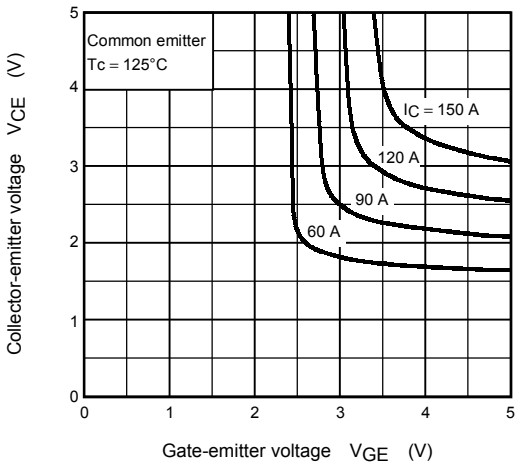
$V_{CE} - V_{GE}$



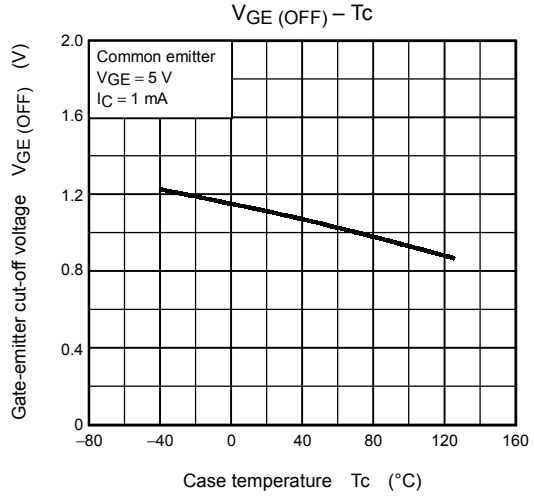
$V_{CE} - V_{GE}$



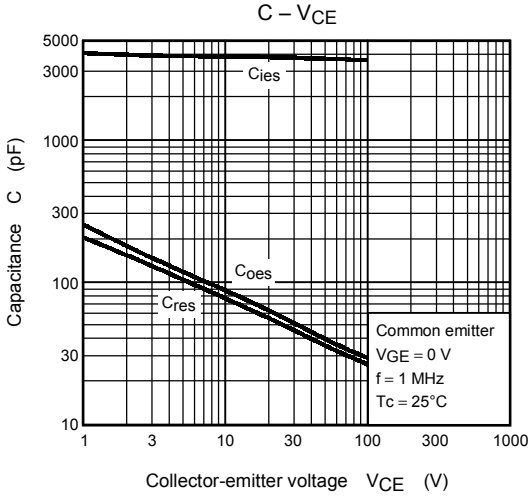
$V_{CE} - V_{GE}$



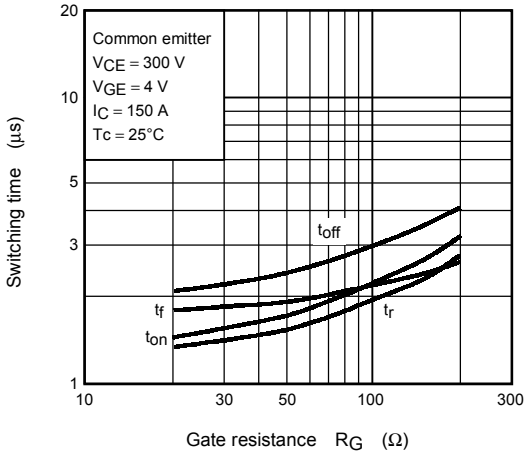
$V_{GE\ (OFF)} - Tc$



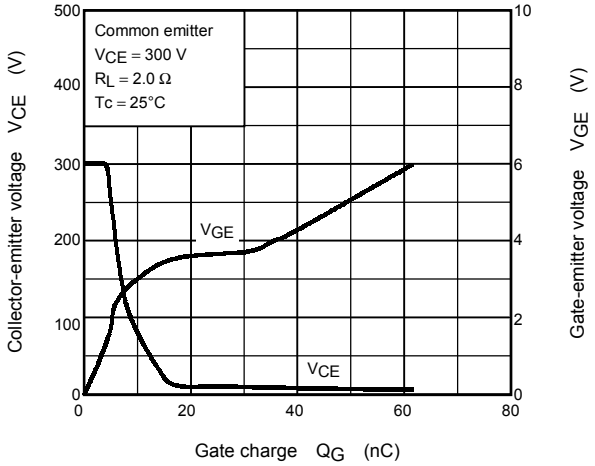
$C - V_{CE}$



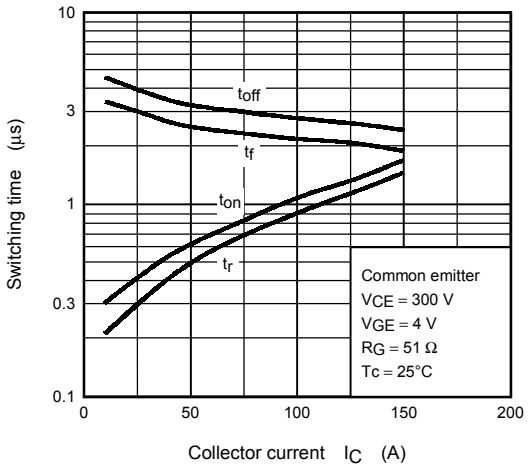
Switching Time – R_G



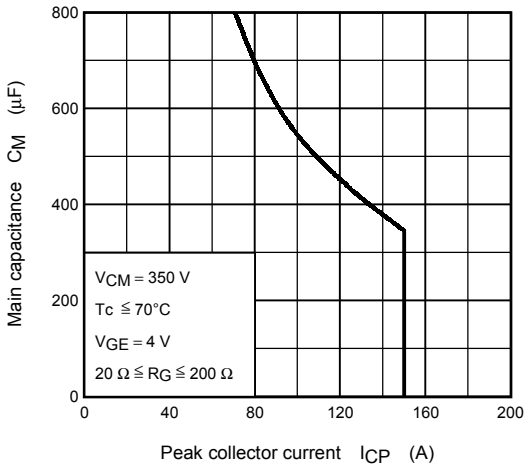
V_{CE}, V_{GE} – Q_G



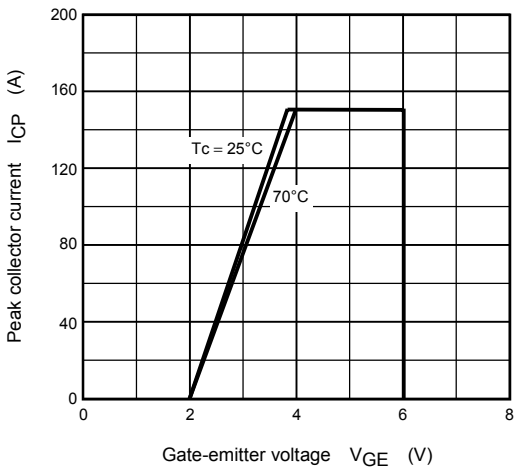
Switching Time – I_C



Maximum Operating Area



Minimum Gate Drive Area



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